Tuesday 13

14:30 Opening
15:00-15:30 "A Dual-Formalism Approach to Checking Consistency of Class and State Diagrams in UML"
by W. Lok Yeung (Lingnan University, Hong Kong)
15:30-16:00 "Reasoning about the Properties of an Enterprise Information System"
by John van der Poll and Paula Kotze
(University of South Africa)
16:00-16:20 "Model Synthesis from Imprecise Specifications"
by Bill Mitchell (University of Surrey, UK),
Robert Thomson and Paul Bristow (Motorola UK)
16:20-17:00 coffee break
17:00-17:30 "Towards a More Intuitive Specification and Automatic Verification of System Properties with FBT"
by Miguel J. Hornos (University of Granada, Spain)
17:30-18:00 "High Level Design Quality Assessment of OO Codes"
by R. Khan and K. Mustafa (Jamia Millia Islamia, India)
18:00-18:20 "Construction of Verified Models for Systems Represented as Networks"
by Agustin Yagüe, Pedro P. Alarcón, Juan Garbajosa
(Universidad Politécnica de Madrid, Spain),
Alexei Lisounkin and Gerhard Schreck
(Fraunhofer-Institute, Germany)

Wednesday 14

09:00-10:20 Invited Talk: "A Generic Framework for Model Checking Software Architectures"
by Radu Mateescu (INRIA Rhône-Alpes/ VASY, France)
10:20-11:00 coffee break
11:00-11:30 "A Multi-stage Approach to Asymmetric Legacy Information Integration"
by Y. Tang, J. Zhang, C. Tan, M. Wong and T. Ng
(Singapore Institute of Manufacturing Technology)
11:30-11:50 "A Hypothesis-Based Approach to Detecting Runtime Violations"
by Lizhang Qin, Xiaoping Jia and Hongming Liu
(DePaul University, USA)
11:50-12:10 "Fault-based Testing of E-Commerce Applications"
by Marisa Sanchez (Universidad del Sur, Argentina),
Juan C. Augusto (University of Ulster, UK) and
Miguel Felder (Pragma Consultores)
Lunch
14:30 Poster session:

* "Verification On The Web Of Mobile Systems"
  (Universitá di Pisa and ISTI-CNR, Italy)
* "New S/Key System against Dictionary Attack : A Case Study in Casper and CSP/FDR"
  by Il-Gon Kim and Jin-Young Choi (Korea University)
* "Collaboration-based Verification of Object-Oriented Models in HOL"
  by Kenro Yatake, Toshiaki Aoki, and Takuya Katayama
  (Japan Advanst Institute of Science and Technology)
* "Sistematic Design of Real-Time Systems Based on CSP+T Process Algebra"
  by Manuel I. Capel, José R. Balsas, and Juan A. Holgado
  (Universidad de Granada and Universidad de Jaén, Spain)
* "Checking Integrity Constraints in Deductive Systems based on Production Rules and a Description Logic Terminology"
  by Jaime Ramírez and Angélica de Antonio
  (Technical University of Madrid, Spain)

15:30-16:00 "Verifying User Interface Behaviour With Model Checking"
  by José Creissac Campos (Universidade do Minho, Portugal), Michael Harrison, and K. Loer (University of York, UK)

16:00 Closing